

## **ABSTRACT OF THE INVENTION**

In one aspect of the invention is a method for reducing crosstalk and maintaining clearances between traces on a printed circuit board design.

Crosstalk caused by placing traces a virtual printed circuit board are reduced by placing artificial obstructions, called spacers, between traces and/or between traces and nets to create a user-specified clearance between the traces and/or nets. As additional traces and/or nets are added to the virtual printed circuit board, the spacers are dynamic and adjust accordingly to maintain the specified clearances.

USPTO Patent Application  
Serial No. 14/822,223  
Docket No. 2282P113  
Filed: 08/05/2015  
First Inventor Name: Srinivasan, Venkateswaran  
Title: Method and System for Reducing Crosstalk and Maintaining Clearances Between Traces on a Printed Circuit Board Design